

REMARKS

35 U.S.C. § 101

In paragraph 2 of the Office action, claims 1-6, 7-9, and 10-15 stand rejected under 35 U.S.C. § 101 because the claims “merely recite the method steps.” In response, claim 1 has been amended to recite, in the body of the claim, “a memory controller” for performing the recited steps. Claim 7 has been amended to recite “wherein said transmitting, simultaneously transmitting, and repeating are performed using a memory controller.” Claim 10 has not been amended but recites transmitting on a selected one of a plurality of “bit lines in said bus,” simultaneously transmitting on one or more of “said plurality of bit lines other than said selected bit line,” and performing a read/write operation “at a data storage location in said memory.” It is believed that in view of that language, each of the independent claims is tied to a particular machine or apparatus. These are not method steps in the abstract but rather method steps performed by a memory controller (claims 1 and 7) or performed over bit lines in combination with a data storage location in a memory (claim 10). In view of these amendments and remarks, it is respectfully requested that the rejection under 35 U.S.C. § 101 be withdrawn.

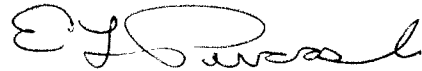
Double Patenting

The double patenting rejection is believed to be overcome by the terminal disclaimer filed herewith.

Appl. No. 10/766,611
Amdt. dated 19 November 2008
Reply to Office action of 19 August 2008

Applicants have made a diligent effort to place the instant application in condition for allowance. Accordingly, a Notice of Allowance for pending claims 1-2, 4-7 and 9-25 is respectfully requested. If the examiner is of the opinion that the instant application is in condition for disposition other than through allowance, the examiner is respectfully requested to contact applicants' attorney at the telephone number listed below.

Respectfully submitted,



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